

(54) Title of the invention : FAST SUBTRACTOR ALGORITHM AND IMPLEMENTATION

<p>(51) International classification :B6W0030180000, H04N0009804000, G06F0007533000, G06F0015167000, G06F0009500000</p> <p>(86) International Application No Filing Date :PCT// :01/01/1900</p> <p>(87) International Publication No : NA</p> <p>(61) Patent of Addition to Application Number Filing Date :NA :NA</p> <p>(62) Divisional to Application Number Filing Date :NA :NA</p>	<p>(71)Name of Applicant :</p> <p><b>1)DR GUNAMANI JENA</b> Address of Applicant :PROFESSOR AND HEAD, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, BVC ENGINEERING COLLEGE (AUTONOMOUS), ODALAREVU, ALLAVARAM MANDAL, EAST-GODHAVARI DISTRICT -----</p> <p><b>2)DR CHANDRA MOULI VSA</b> <b>3)DR T V JANARDHANA RAO</b> <b>4)Mr SHUBHASHISH JENA</b> <b>5)MS. SUBHASHREE JENA</b> <b>6)MRS P DEEPA</b> <b>7)DR DEVABALAN POUNRAJ</b> Name of Applicant : NA Address of Applicant : NA</p> <p>(72)Name of Inventor :</p> <p><b>1)DR GUNAMANI JENA</b> Address of Applicant :PROFESSOR AND HEAD, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, BVC ENGINEERING COLLEGE (AUTONOMOUS), ODALAREVU, ALLAVARAM MANDAL, EAST-GODHAVARI DISTRICT -----</p> <p><b>2)DR CHANDRA MOULI VSA</b> Address of Applicant :PRINCIPAL, BVC ENGINEERING COLLEGE (AUTONOMOUS), ODALAREVU, ALLAVARAM MANDAL, EAST-GODHAVARI DISTRICT -----</p> <p><b>3)DR T V JANARDHANA RAO</b> Address of Applicant :DEAN AND PROFESSOR, BVC ENGINEERING COLLEGE (AUTONOMOUS), ODALAREVU, ALLAVARAM MANDAL, EAST-GODHAVARI DISTRICT, -----</p> <p><b>4)Mr SHUBHASHISH JENA</b> Address of Applicant :SOFTWARE ENGINEER(DEVOPS) &amp; SOC ANALYST, MONSTER.COM(INDIA) PVT. LTD, HYDERABAD -----</p> <p><b>5)MS. SUBHASHREE JENA</b> Address of Applicant :PH.D., RESEARCH SCHOLAR, DEPARTMENT OF MARINE BIOLOGY AND OCEANOGRAPHY, ANNAMALAI UNIVERSITY, CHIDAMBARAM, -----</p> <p><b>6)MRS P DEEPA</b> Address of Applicant :PH.D., RESEARCH SCHOLAR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, ANNAMALAI UNIVERSITY, CHIDAMBARAM -----</p> <p><b>7)DR DEVABALAN POUNRAJ</b> Address of Applicant :PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, BVC ENGINEERING COLLEGE (AUTONOMOUS), ODALAREVU, ALLAVARAM MANDAL, EAST-GODHAVARI DISTRICT -----</p>
---	--

(57) Abstract :  
Fast subtractor algorithm and implementation based on borrow look ahead is the proposed invention. The general condition for minimum time delay in the subtractor has been used to obtain the required result. It passes through 4-gate delay irrespective of number size. In the implementation, high speed is achieved sacrificing hardware cost.

No. of Pages : 13 No. of Claims : 2